

**UNIVERSITY COLLEGE OF ENGINEERING  
OSMANIA UNIVERSITY, HYDERABAD - 7**

**M. E. (ECE, Mechanical & CSE) II - Semesters (Main) Examination  
July/August 2016**

**M V S R ENGINEERING COLLEGE**

**Re-REVISED EXAMINATION TIME-TABLE**

**Time : 2.00 PM to 5.00 PM**

DATE & DAY	E. C. E. (EMBEDDED SYSTEMS & VLSI DESIGN)	MECHANICAL (CAD / CAM)	C. S. E.
	II-Semester	II-Semester	II-Semester
25-07-2016 MONDAY	VLSI Physical Design	Flexible Manufacturing Systems / Vibration Analysis & Condition Monitoring	Cloud Computing
27-07-2016 WEDNESDAY	Scripting Languages for VLSI Design Automation	Automation	Advanced Databases
29-07-2016 FRIDAY	Low Power VLSI Design	Additive Manufacturing Technologies & Applications	=====
02-08-2016 TUESDAY	Real Time Operating Systems	Control of Dynamic Systems / Design for Manufacture	Distributed Computing
04-08-2016 THURSDAY	<b>Digital Signal Processors</b>	=====	Machine Learning
06-08-2016 SATURDAY	Digital IC Design	Computer Aided Modelling & Design	Image Processing
08-08-2016 MONDAY	Field Programmable Gate Arrays	Non-Traditional Machining & Forming	=====
10-08-2016 WEDNESDAY	Advanced Computer Organization	Finite Element Techniques	Network Security



**DIRECTOR OF EVALUATION  
EXAMINATION CELL**